CC3501 Weekly Report

**Group number:** 2 **Team members:** Ethan Waters, Lachlan Pryce  
**Week number:** 7

**Progress this week**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Task** | **Who did it?** | **What were the outcomes?** | **Who did the peer review?** | **What did you learn?** |
| Check logical output voltage levels for all CAN bus hardware | Ethan | CAN bus transceiver and controller logical levels are 3V3 | Lachlan | Can not assume logical output voltages are 3V3 |
| Increase track width of power rail | Ethan | Ensure sufficient current carrying capacity | Lachlan |  |
| Reroute due to design changes | Ethan |  |  |  |
| Remove termination for every CAN bus node and implement header pins to terminate ends of CAN bus with 100ohm | Ethan | Termination more appropriate for design |  | More about CAN bus standards and functionality |
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**Overall project tracking:** [fill this in at the beginning of the project and update weekly based on actual progress]

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| --- | --- |
| **Week number** | **Milestones** |
| 1 | Confirm project topic and begin |
| 2 |  |
| 3 | Arm can move with an input from a socket. The input is an automated test script executed by a client to mimic the embedded system output |
| 4 | Select components & review datasheets |
| 5 | Begin schematic |
| 6 | Complete Schematic, forward to Bronson for feedback. |
| 7 | Submit complete schematic to Bronson for feedback. Complete PCB design for feedback, |
| 8 | Submit board for manufacture. |
| 9 | Work on vision based movement with PI while waiting for embedded systems | Any other code that can be done before |
| LR |  |
| 10 |  |
| 11 |  |
| 12 |  |
| 13 | Demo day during Friday lab |